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		Application Number	09/838,552
Sheet 2 of 2		Filing Date	April 18, 2001
		First Named Inventor	Babaian, Boris A.
		Group Art Unit	2182 2122
		Examiner Name	
		Attorney Docket Number	020181-004900US

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>mf</i>	AD ✓	SITES, RICHARD L., et al., "Binary Translation," Communications of the ACM, February 1993, Vol. 36, No. 2, pages 69-81.	<input type="checkbox"/>
<i>mf</i>	AE	DOBBERPUHL, DANIEL, et al., "Design of EV-4," Communications of the ACM, February 1993, Vol. 36, No. 2, page 82. (one-page - stapled to back of Cite AD above)	<input type="checkbox"/>
<i>mf</i>	AF ✓	DIEFENDORFF, KEITH, "The Russians are Coming - Supercomputer Maker Elbrus Seeks to Join x86/1A-64 Melee," Microprocessor Report, February 15, 1999, Vol. 13, No. 2, pages 1-7.	<input type="checkbox"/>
<i>mf</i>	AG ✓	SILBERMAN, GABRIEL M., et al., "An Architectural Framework for Supporting Heterogeneous Instruction-Set Architectures," Computer, June 1993, Vol. 26, No. 6, pages 39-56.	<input type="checkbox"/>
<i>mf</i>	AH ✓	RIVEST, RONALD L., "The MD5 Message-Digest Algorithm," Memo, Network Working Group, MIT Laboratory for Computer Science and RSA Data Security, Inc., April 1992, 21 pages.	<input type="checkbox"/>
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Examiner Signature	<i>M. Stetson</i>	Date Considered	7-6-04
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